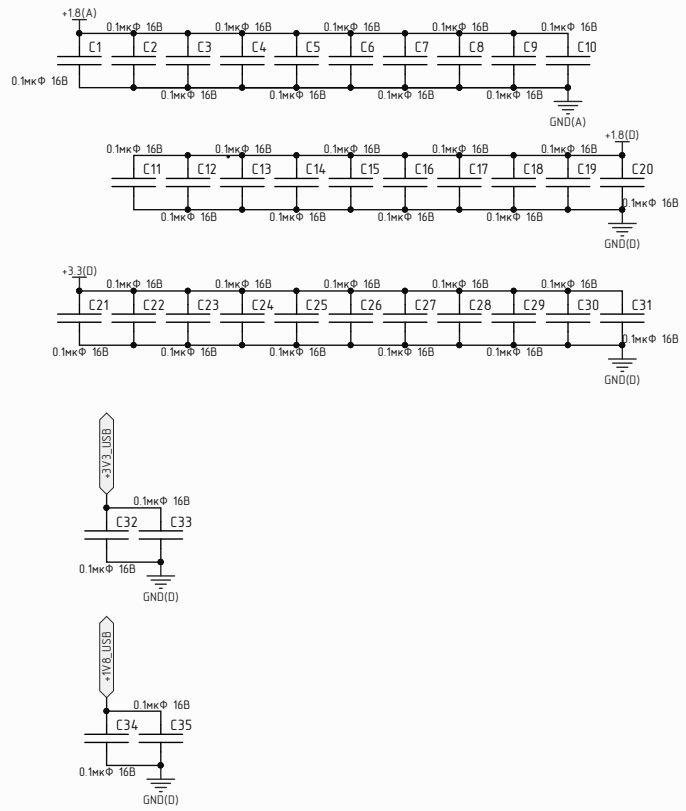
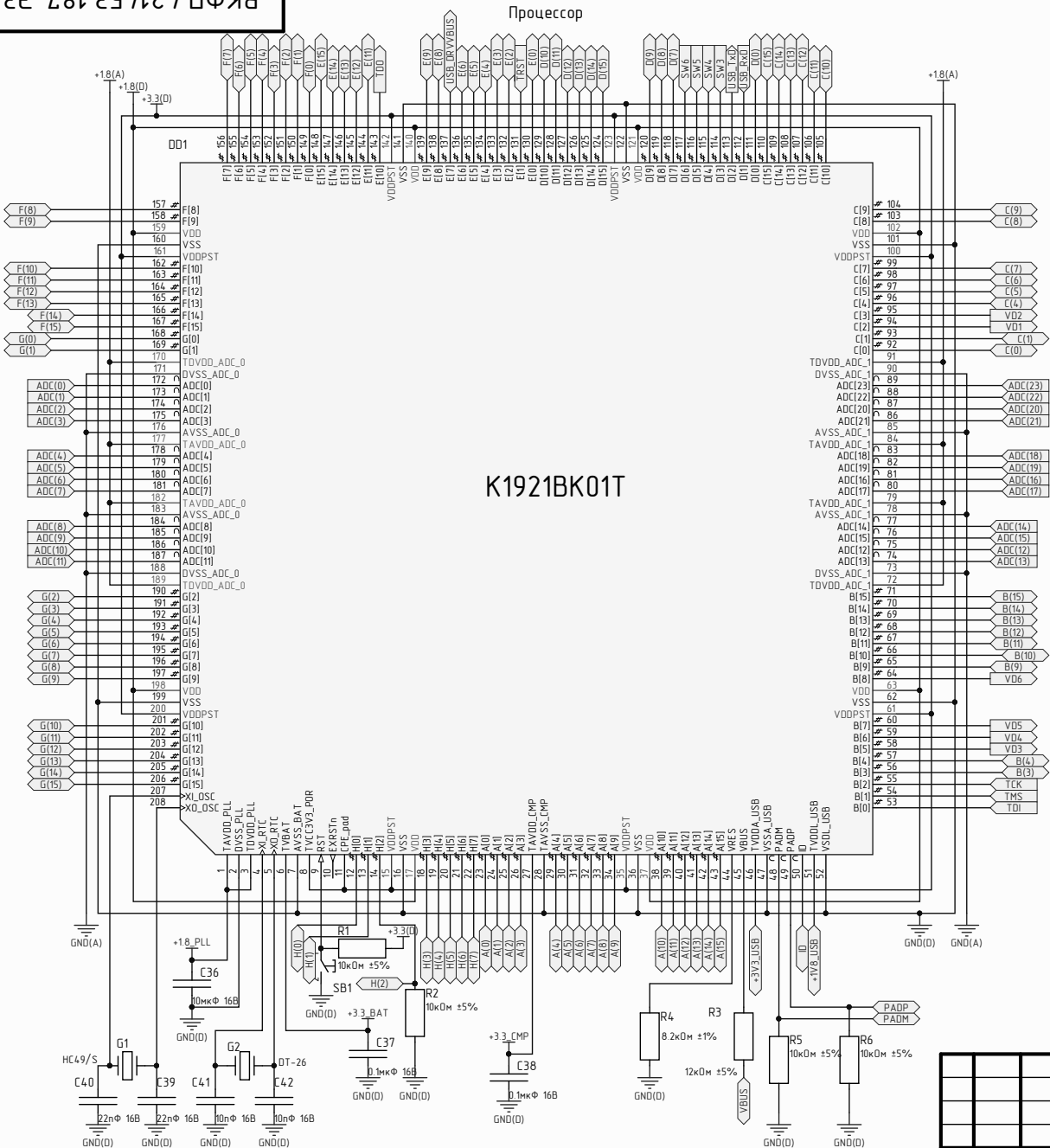


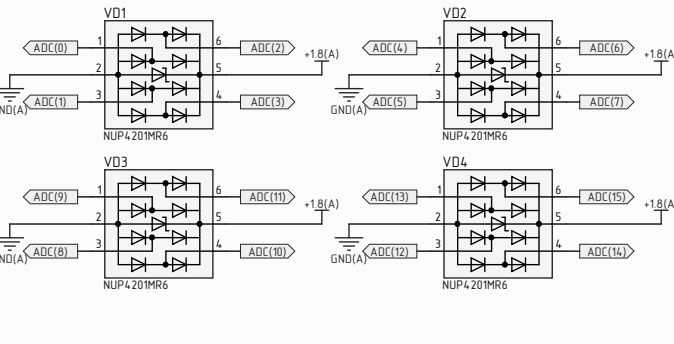
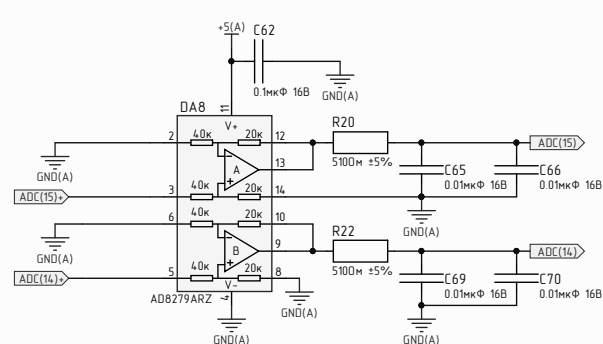
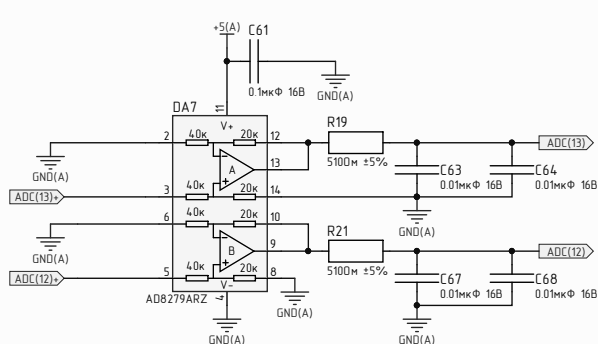
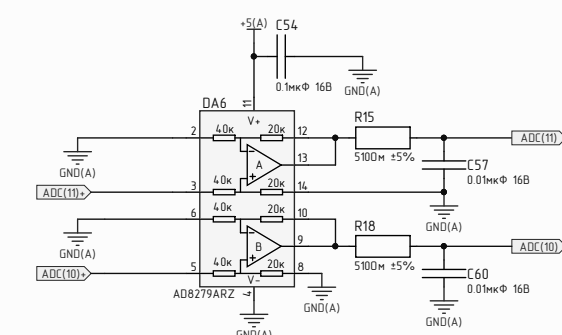
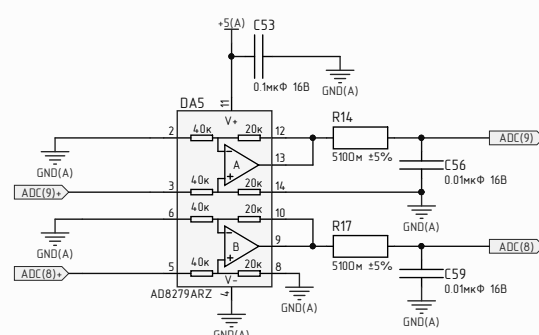
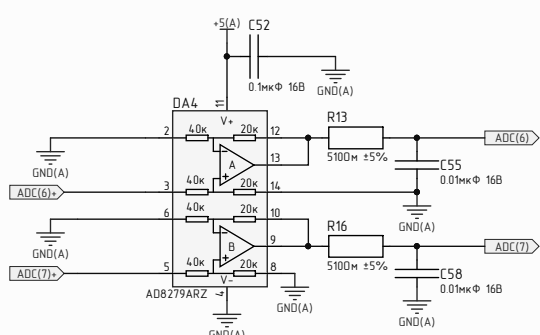
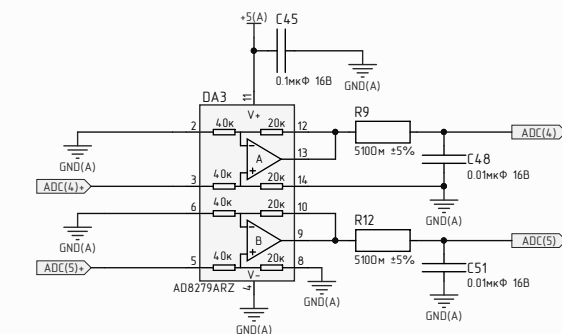
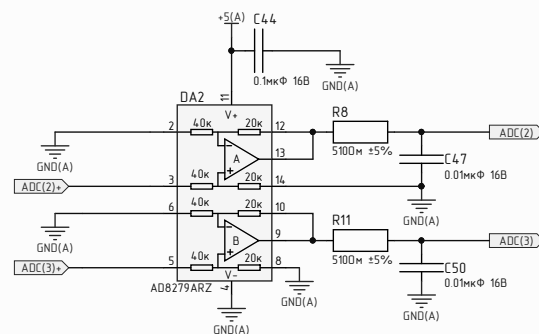
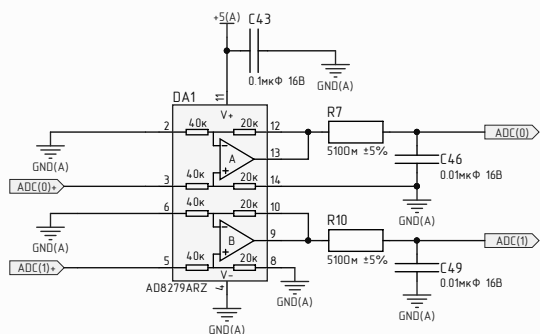
БКФП.421453.187 ЭЗ



Плата печатная VectorCard
БКФП.758755.216

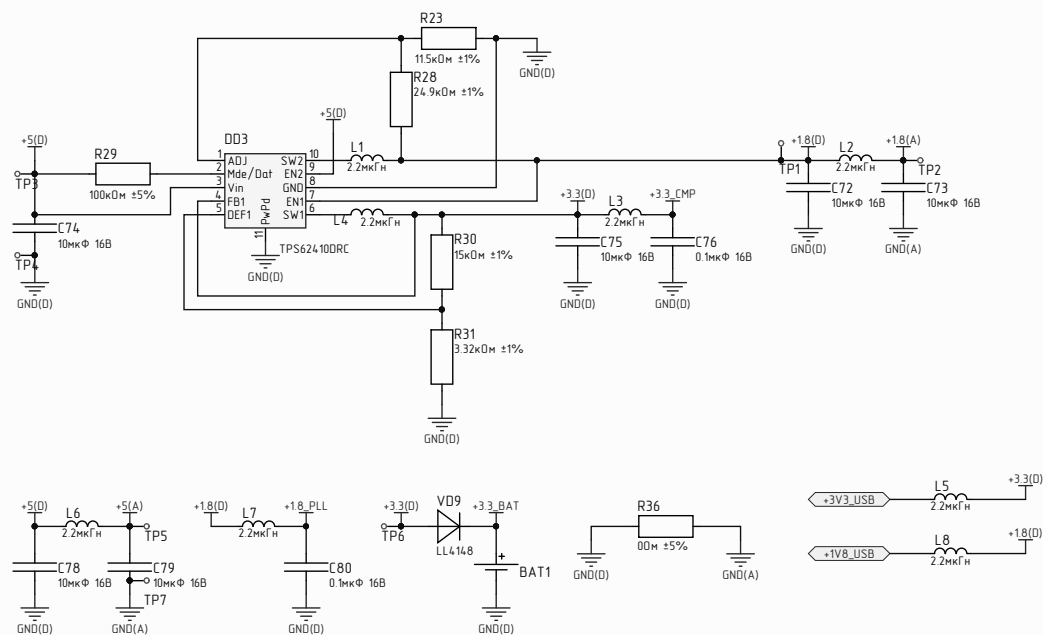
					ВКФП.421453.187 ЭЗ						
					Отладочная плата VectorCARD K1921BK01T	Лист.			Масса	Масштаб	
Изм.	Лист	№ докум.	Подп.	Дата						1:1	
Разраб.		Двоеглазов									
Пров.		Клюев									
Т.контр.		Обухов			Схема электрическая принципиальная	Лист	1	Листов	5		
						НПФ ВЕКТОР					
И.контр.		Жарков									
Утв.		Анучин									

Цепи АЦП

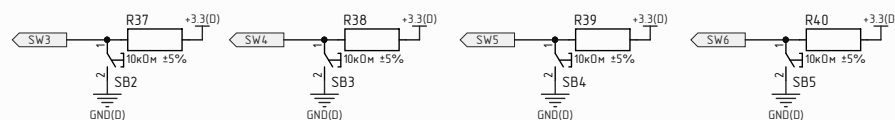


Защиты

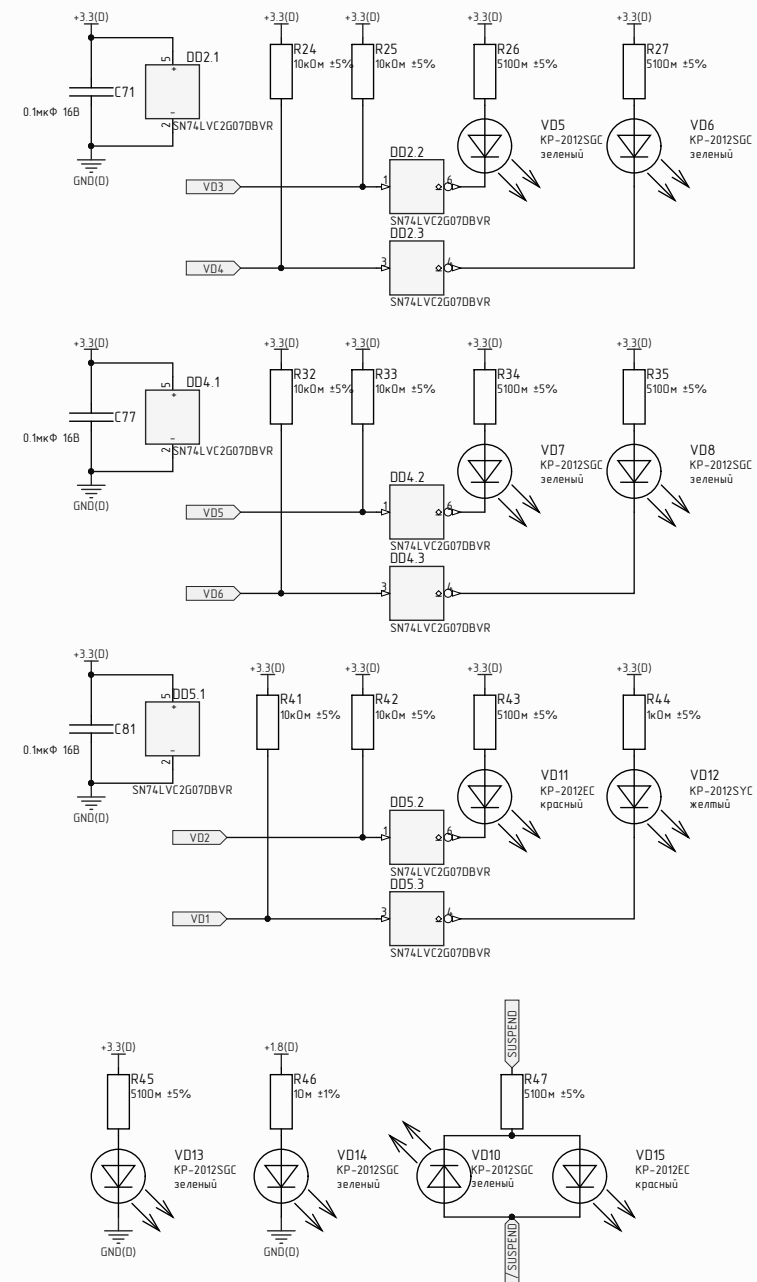
Питание

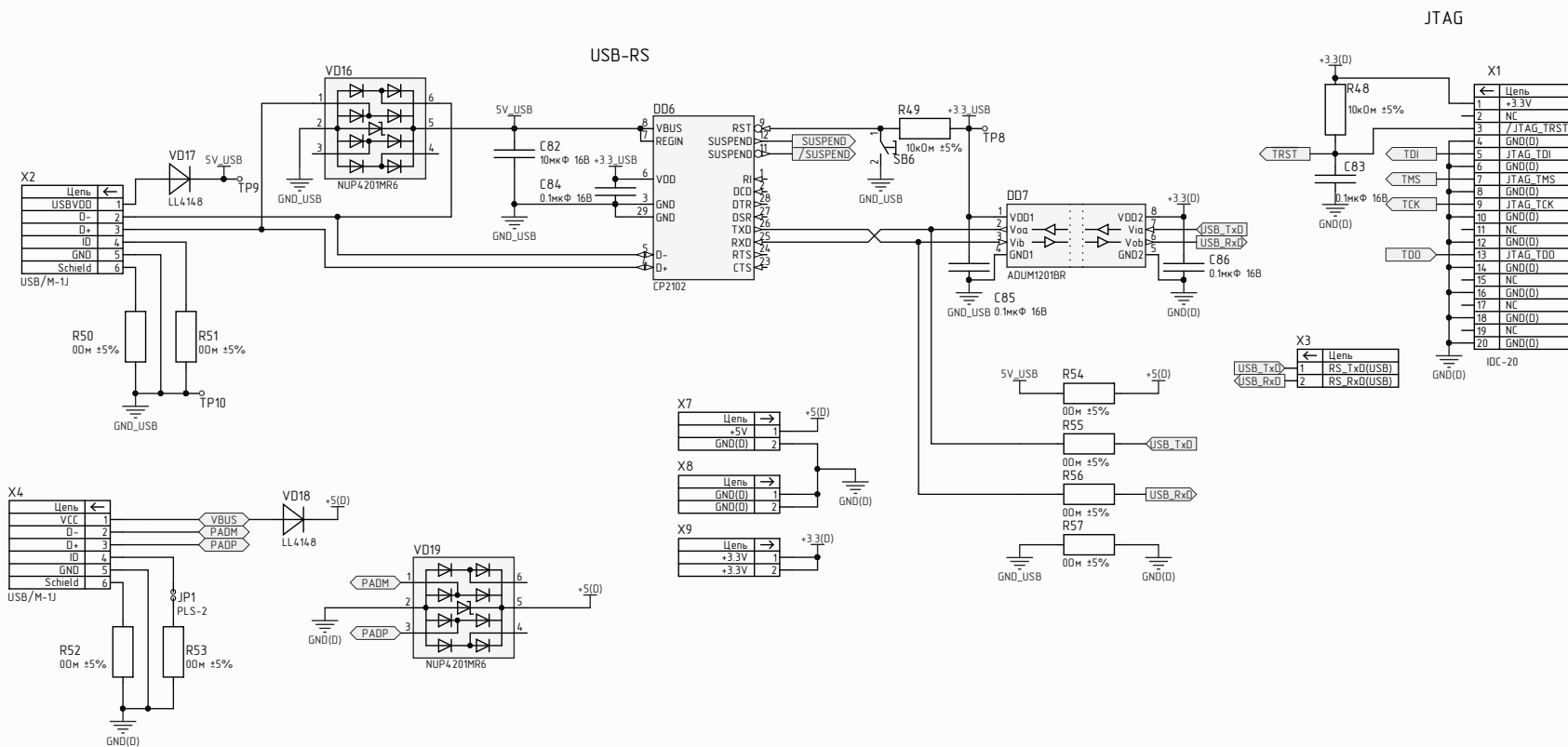


Управление



Индикация

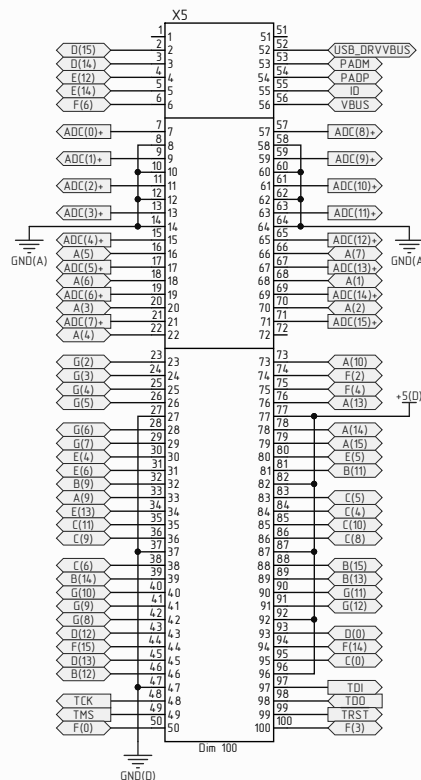




Разъем DIMM-100

Канал процессора

RAM_DATA[14] / MLCQ_D[15]
RAM_DATA[13] / MLCQ_D[14]
RAM_DATA[15] / ePWM_TZ[3] / MLCQ_D[12]
RAM_Qen[0] / ePWM_TZ[5] / MLCQ_D[11]
RAM_Cen[0] / eCMP_OUT[0] / MLCQ_D[10]
ADC(0)
GND
ADC(1)
GND
ADC(2)
GND
ADC(3)
GND
ADC(4)
SSP_FSS[0] / ePWMxA[7] / trace_dat[0] / C1- A[5]
SSP_CLK[0] / ePWMxB[7] / trace_dat[1] / C1- A[6]
UART_TxD[1] / ePWMxA[6] / RAM_Qen[1] / C2- A[3]
UART_RxD[1] / ePWMxB[6] / trace_dat[2] / C2- A[4]
ePWMxA[0] / UART_DCD[0] / SSP_RxD[1] G[2]
ePWMxA[1] / SSP_FSS[3] / CAN_TX[1] G[3]
ePWMxA[2] / SSP_RxD[3] / UART_TxD[2] G[4]
ePWMxA[3] / SSP_CLK[2] / RAM_DATA[13] G[5]
GND
ePWMxA[4] / SSP_TxD[2] / RAM_DATA[15] G[6]
CAN_TX[0] / ePWMxA[5] / RAM_DATA[15] G[7]
eQEPxA/CLK[0] / eCAPxPWM[0] / Timer_IN[0] F[4]
eQEPX[0] / eCAPxPWM[2] / RAM_DATA[8] B[9]
RAM_ADDR[13] / MLCQ_D[1] / ePWM_SYNC B[9]
RAM_ADDR[1] / MLCQ_D[1] / ePWM_TZ[1] A[9]
RAM_Wen / ePWM_TZ[4] / MLCQ_D[1] F[13]
RAM_DATA[6] / SSP_TxD[0] / MLCQ_D[1] F[11]
RAM_DATA[4] / SSP_CLK[0] / ePWM_TZ[5] C[9]
GND
RAM_DATA[1] / UART_RxD[1] / SSP_TxD[2] C[6]
RAM_ADDR[18] / CAN_TX[0] / SSP_CLK[2] B[14]
eCAPxPWM[3] / eQEPxA/CLK[1] G[10]
ePWM_SYNC / eQEPXS[0] / UART_RTS[3] G[9]
Timer_IN[1] / UART_DSR[2] / CAN_RX[1] G[8]
RAM_DATA[11] / UART_TxD[2] / MLCQ_D[1] F[15]
CAN_RX[1] / UART_DCD[2] / UART_RTS[1] F[15]
RAM_DATA[12] / MLCQ_D[1] F[13]
RAM_ADDR[16] / MLCQ_DV / eCMP_OUT[2] B[12]
GND
ITAG_TCK/SWCLK / SSP_RxD[1] / RAM_Cen[1] B[2]
ITAG_TMS/SWDIO / SSP_CLK[1] / RAM_Cen[0] B[1]
ePWMxB[0] / UART_DSR[0] / SSP_TxD[1] F[0]



Канал процессора

F[7] USB_DRVVBUS / eQEPXS[0] / ePWM_TZ[3]
PADM
PADP
ID
VBUS
ADC(8)
GND
ADC(9)
GND
ADC(10)
GND
ADC(11)
GND
ADC(12)
A[7] SSP_RxD[0] / ePWMxA[8] / trace_clk / DAC Supply
ADC(13)
A[1] SSP_TxD[0] / ePWMxB[8] / trace_dat[3] / C3-
ADC(14)
A[2] ePWM_TZ[2] / SSP_TxD[1] / RAM_Qen[0] / C3-
ADC(15)
A[10] RAM_ADDR[2] / MLCQ_D[1] / ePWMxB[0]
F[2] ePWMxB[1] / SSP_CLK[3] / CAN_RX[0]
F[4] ePWMxB[2] / SSP_TxD[3] / UART_RxD[2]
A[13] RAM_ADDR[5] / MLCQ_DN / ePWMxB[3]
+5B
A[14] RAM_ADDR[6] / MLCQ_ER / ePWMxB[4]
A[15] RAM_ADDR[7] / MLCQ_CS / ePWMxB[5]
F[5] eQEPxB/XQIR[0] / eCAPxPWM[1] / RAM_LBn
B[11] RAM_ADDR[15] / MLCQ_D[3] / eCMP_OUT[1]
+5B
C[5] MLCQ_ER / UART_TxD[2] / RAM_ADDR[6]
C[4] MLCQ_EN / UART_RxD[0] / RAM_ADDR[5]
C[10] RAM_DATA[5] / SSP_RxD[0] / MLCQ_CLK
C[8] RAM_DATA[3] / SSP_FSS[0] / ePWM_TZ[4]
+5B
B[15] RAM_DATA[0] / CAN_RX[0] / SSP_RxD[2]
B[13] RAM_ADDR[17] / MLCQ_ER / SSP_FSS[2]
G[11] eCAPxPWM[4] / eQEPxB/XQIR[1] / Timer_IN[1]
G[12] eCAPxPWM[5] / eQEPX[1] / Timer_IN[2]
+5B
C[7] RAM_DATA[2] / UART_TxD[1] / ePWM_TZ[2]
F[14] CAN_TX[1] / UART_RTS[2] / UART_DTR[1]
C[0] MLCQ_D[0] / -- / RAM_ADDR[1]
+5B
B[0] ITAG_TD / SSP_FSS[1] / RAM_Wen
F[0] ITAG_TD/SWD / ePWM_SYNC / RAM_LBn
F[1] ITAG_TRST / sda[0] / UART_TxD[2]
F[3] ePWMxB[3] / SSP_RxD[2] / RAM_DATA[14]

