

Port B

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
PB0		TIMER0_C H1_ON	TIMER2_C H2	TIMER7_C H1_ON				SPI2_MOSI /I2S2_SD			USBHS_U LPI_D1	ENET_MII_ RXD2	SDIO_D 1			EVENTOUT
PB1		TIMER0_C H2_ON	TIMER2_C H3	TIMER7_C H2_ON							USBHS_U LPI_D2	ENET_MII_ RXD3	SDIO_D 2			EVENTOUT
PB2		TIMER1_C H3						SPI2_MOSI /I2S2_SD			USBHS_U LPI_D4		SDIO_C K			EVENTOUT
PB3	JTDO/TRAC ESWO	TIMER1_C H1				SPI0_SCK	SPI2_SCK /I2S2_CK	USART0_R X		I2C1_SDA						EVENTOUT
PB4	NJTRST		TIMER2_C H0		I2C0_TFX RAME	SPI0_MIS O	SPI2_MIS O	I2S2_ADD_ SD		I2C2_SDA			SDIO_D 0			EVENTOUT
PB5			TIMER2_C H1		I2C0_SMB A	SPI0_MO SI	SPI2_MO SI/I2S2_S D			CAN1_RX	USBHS_U LPI_D7	ENET_PPS _OUT	EXMC_S DCKE1	DCI_D10		EVENTOUT
PB6			TIMER3_C H0		I2C0_SCL			USART0_T X		CAN1_TX			EXMC_S DNE1	DCI_D5		EVENTOUT
PB7			TIMER3_C H1		I2C0_SDA			USART0_R X					EXMC_N L/EXMC _NADV	DCI_VSY NC		EVENTOUT
PB8		TIMER1_C H0/TIMER 1_ETI	TIMER3_C H2	TIMER9_C H0	I2C0_SCL					CAN0_RX		ENET_MII_ TXD3	SDIO_D 4	DCI_D6		EVENTOUT
PB9		TIMER1_C H1	TIMER3_C H3	TIMER10_ CH0	I2C0_SDA	SPI1_NSS /I2S1_WS				CAN0_TX			SDIO_D 5	DCI_D7		EVENTOUT
PB10		TIMER1_C H2			I2C1_SCL	SPI1_SCK /I2S1_CK	I2S2_MCK	USART2_T X			USBHS_U LPI_D3	ENET_MII_ RX_ER	SDIO_D 7			EVENTOUT
PB11		TIMER1_C H3			I2C1_SDA	I2S_CKIN		USART2_R X			USBHS_U LPI_D4	ENET_MII_ TX_EN/ENE T_RMII_TX_ EN				EVENTOUT
PB12		TIMER0_B RKIN			I2C1_SMB A	SPI1_NSS /I2S1_WS		USART2_C K		CAN1_RX	USBHS_U LPI_D5	ENET_MII_ TXD0/ENET _RMII_TXD 0	USBHS_ ID			EVENTOUT
PB13		TIMER0_C H0_ON			I2C1_TFX RAME	SPI1_SCK /I2S1_CK		USART2_C TS		CAN1_TX	USBHS_U LPI_D6	ENET_MII_ TXD1/ENET _RMII_TXD 1				EVENTOUT
PB14		TIMER0_C H1_ON		TIMER7_C H1_ON		SPI1_MIS O	I2S1_ADD_ SD	USART2_R TS		TIMER11_ CH0			USBHS_ DM			EVENTOUT
PB15	RTC_REFI N	TIMER0_C H2_ON		TIMER7_C H2_ON		SPI1_MO SI/I2S1_S D				TIMER11_ CH1			USBHS_ DP			EVENTOUT

Port D

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
PD0							SPI2_MOSI/I2S2_SD			CAN0_RX			EXMC_D2			EVENTOUT
PD1								SPI1_NSS/I2S1_WS		CAN0_TX			EXMC_D3			EVENTOUT
PD2			TIMER2_ETI						UART4_RX				SDIO_CMD	DCI_D11		EVENTOUT
PD3						SPI1_SCK/I2S1_CK		USART1_CTS					EXMC_CLK	DCI_D5		EVENTOUT
PD4								USART1_RTS					EXMC_NOE			EVENTOUT
PD5								USART1_TX					EXMC_NWE			EVENTOUT
PD6						SPI2_MOSI/I2S2_SD		USART1_RX					EXMC_NWAIT	DCI_D10		EVENTOUT
PD7								USART1_CK					EXMC_NEO/EXMC_NCE1			EVENTOUT
PD8								USART2_TX					EXMC_D13			EVENTOUT
PD9								USART2_RX					EXMC_D14			EVENTOUT
PD10								USART2_CK					EXMC_D15			EVENTOUT
PD11								USART2_CTS					EXMC_A16/EXMC_CLE			EVENTOUT
PD12			TIMER3_CH0					USART2_RTS					EXMC_A17/EXMC_ALE			EVENTOUT
PD13			TIMER3_CH1										EXMC_A18			EVENTOUT
PD14			TIMER3_CH2										EXMC_D0			EVENTOUT
PD15	CTC_SYNC		TIMER3_CH3										EXMC_D1			EVENTOUT

Port E

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
PE0			TIMER3_ETI										EXMC_NBL0	DCI_D2		EVENTOUT
PE1		TIMER0_C H1_ON											EXMC_NBL1	DCI_D3		EVENTOUT
PE2												ENET_MII _TXD3	EXMC_A2 3			EVENTOUT
PE3													EXMC_A1 9			EVENTOUT
PE4													EXMC_A2 0	DCI_D4		EVENTOUT
PE5				TIMER8_C H0									EXMC_A2 1	DCI_D6		EVENTOUT
PE6				TIMER8_C H1									EXMC_A2 2	DCI_D7		EVENTOUT
PE7		TIMER0_ETI											EXMC_D4			EVENTOUT
PE8		TIMER0_C H0_ON											EXMC_D5			EVENTOUT
PE9		TIMER0_C H0											EXMC_D6			EVENTOUT
PE10		TIMER0_C H1_ON											EXMC_D7			EVENTOUT
PE11		TIMER0_C H1											EXMC_D8			EVENTOUT
PE12		TIMER0_C H2_ON											EXMC_D9			EVENTOUT
PE13		TIMER0_C H2											EXMC_D1 0			EVENTOUT
PE14		TIMER0_C H3											EXMC_D1 1			EVENTOUT
PE15		TIMER0_B RKIN											EXMC_D1 2			EVENTOUT

Port F

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
PF0	CTC_SYN C				I2C1_SDA								EXMC_A0			EVENTOU T
PF1					I2C1_SCL								EXMC_A1			EVENTOU T
PF2					I2C1_SMB A								EXMC_A2			EVENTOU T
PF3					I2C1_TXF RAME								EXMC_A3			EVENTOU T
PF4													EXMC_A4			EVENTOU T
PF5													EXMC_A5			EVENTOU T
PF6				TIMER9_C H0									EXMC_NI ORD			EVENTOU T
PF7				TIMER10_ CH0									EXMC_NR EG			EVENTOU T
PF8										TIMER12_ CH0			EXMC_NI OWR			EVENTOU T
PF9										TIMER13_ CH0			EXMC_CD			EVENTOU T
PF10													EXMC_IN TR	DCI_D11		EVENTOU T
PF11													EXMC_SD NRAS	DCI_D12		EVENTOU T
PF12													EXMC_A6			EVENTOU T
PF13													EXMC_A7			EVENTOU T
PF14													EXMC_A8			EVENTOU T
PF15													EXMC_A9			EVENTOU T

Port G

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
PG0													EXMC_A1 0			EVENTOU T
PG1													EXMC_A1 1			EVENTOU T
PG2													EXMC_A1 2			EVENTOU T
PG3													EXMC_A1 3			EVENTOU T
PG4													EXMC_A1 4			EVENTOU T
PG5													EXMC_A1 5			EVENTOU T
PG6													EXMC_IN T1	DCI_D12		EVENTOU T
PG7									USART5_ CK				EXMC_IN T2	DCI_D13		EVENTOU T
PG8									USART5_ RTS			ENET_PP S_OUT	EXMC_SD CLK			EVENTOU T
PG9									USART5_ RX				EXMC_NE 1/EXMC_N CE2	DCI_VSYN C		EVENTOU T
PG10													EXMC_NC E3_0/EXM C_NE2	DCI_D2		EVENTOU T
PG11												ENET_MII _TX_EN/E NET_RMII _TX_EN	EXMC_NC E3_1	DCI_D3		EVENTOU T
PG12									USART5_ RTS				EXMC_NE 3			EVENTOU T
PG13									USART5_ CTS			ENET_MII _TXD0/EN ET_RMII_ TXD0	EXMC_A2 4			EVENTOU T
PG14									USART5_ TX			ENET_MII _TXD1/EN ET_RMII_ TXD1	EXMC_A2 5			EVENTOU T
PG15									USART5_ CTS				EXMC_SD NCAS	DCI_D13		EVENTOU T

